



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Method for Making MOSFET Anti-Fuse Structure						
Application Number:		10/773263					
Confirmation Number:		8570					
First Named Applicant:		Akira ITO					
Attorney Docket Number:		1875.2590001					
Art Unit:		2813					
Examiner:		Jennifer M. Dolan					
Search string:		(6700176).pn.					
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6700176	2004-03-02	Ito et al.			
Signature							
Examiner Name				Date			
				11/15/04			